

Notice of References Cited	Application/Control No. 10/813,615	Applicant(s)/Patent Under Reexamination KNOWLES, SIMON	
	Examiner Ryan P. Fiegler	Art Unit 2183	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,423,051	06-1995	Fuller et al.	712/7
*	B	US-2005/0044434	02-2005	Kahle et al.	713/320
	C	US-			
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	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

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	U	Bolotski, Michael et al. "Unifying FPGAs and SIMD Arrays." MIT: 1994.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.